


Ashish S. Deshmukh, M.E. (Digital Electronics)

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Male, born on Apr 15, 1975, Married
Languages known: English, Marathi, Hindi

CAREER OBJECTIVE

- Use of exceptional interpersonal, communication, computer, technical and teaching skills.
- Provide a mixture of hard and soft skills in your resume objective and skills sections.

HIGHLIGHTS

1. Having about Total 18 years Teaching, Academic administration in Government and Private sector.
2. Out of which about 17 years of Teaching Experience at Government Diploma Level Institutes in State of Maharashtra.
3. Half year Experience as a Technical Executive in Reliance mobile, Mumbai.
4. Total 3 Review and Research Papers published in National and International journals and Conferences

EXPERIENCE

- 1 August 2011-Till Date, Lecturer, Government Polytechnic Gadchiroli, Maharashtra (India).
- 2 January 2004 - July 2011, Lecturer, Government Polytechnic Bramhapuri, Maharashtra (India).
- 3 August 2003 - January 2004, Technical Executive in Reliance mobile, Mumbai. Maharashtra (India).
- 4 July 2002 - August 2003 Lecturer, Government Polytechnic Bramhapuri, Maharashtra (India).
- 5 Aug 2001 - July 2002 Lecturer, Government Polytechnic Gadchiroli, Maharashtra (India).

ACADEMIC QUALIFICATION

- 2017, M. E. (Digital Electronics) Sant Gadge Baba Amravati University, Amravati.
- 2000, BE (Electronics Engg.) Rashtrasant Tukdogi Maharaj Nagpur University ,Nagpur.

PUBLICATIONS

Journal Paper Publications (02 International)

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1. Ashish S. Deshmukh, Prof. Vinay U. Kale, “Design of High Speed ADC for Digital Communication: A Survey”, Published in, SSRG International Journal of Electronics and communication Engineering (SSRG-IJECE), ISSN: 2348 – 8549, Part-1, 2016
2. Ashish S. Deshmukh, Prof. Vinay U. Kale, “Design of High Speed ADC for Digital Communication”, Published in TECH-CRONICAL, An International E-Journal on emerging trends in science technology and management, ISSN NO: 2454-1958, Special Issue Tech-Ed 2017

Conference Paper Publications (02 International and 01 National)

1. Ashish S. Deshmukh, Prof. Vinay U. Kale, “Design of High Speed ADC for Digital Communication: A Survey”, Published in, Satellite Conference **ICSTSD** 2016 International Conference on Science and Technology for Sustainable Development, Kuala Lumpur, MALAYSIA, May 24-26, 2016.
2. Ashish S. Deshmukh, Prof. Vinay U. Kale, “Design of High Speed ADC for Digital Communication”, Published in Third annual International **TechEd** Conference Organized by Tulshiramji Gayakwad-Patil College of Engg and Technology Nagpur ,Maharashtra. February 17-18,2017.
3. Ashish S. Deshmukh, “Classification & Recognition Methods on Microscopic Images”, Published in National Conference on recent innovations in science, engineering and technology **NCRISSET**, Organized by G.H. Raisonni Institute of Engg and Technology Nagpur ,Maharashtra,2017.